

Modular Specification and Verification of Delegation with SMT Solvers



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1ST HOPE WORKSHOP, 9/9 COPENHAGEN

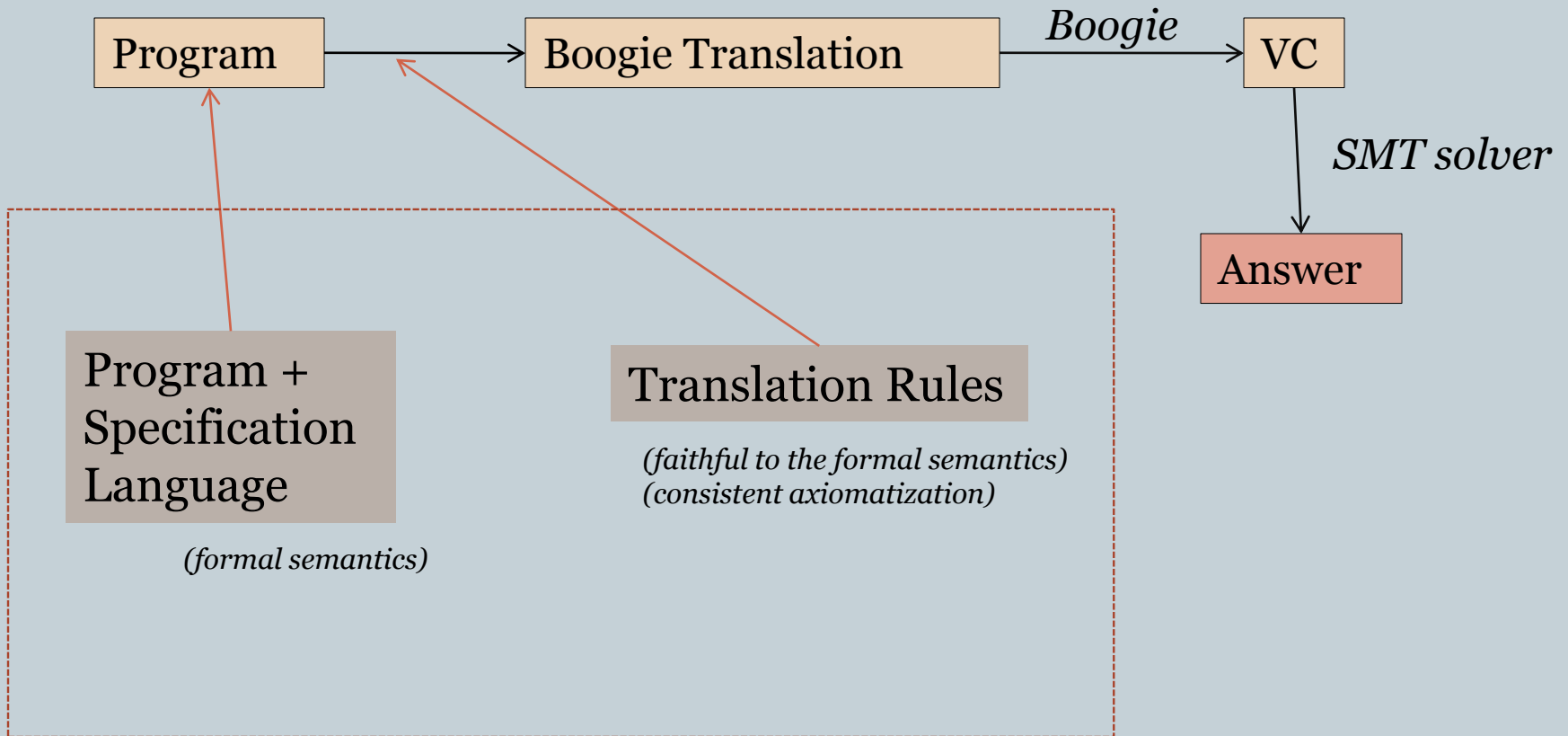
Problem Overview

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- **Modular Automated Verification**
 - all specification annotation comes with the program
 - no user-computer interaction during verification
 - each module verified separately
 - examples: Boogie, Chalice, Dafny, Spec#, VCC, Why,.....
 - typically uses SMT solvers → first order logic + theories
- **Delegation**
 - use of statically unknown code (e.g., closures)
 - essential ingredient in many design patterns
 - typically verified as a higher order logic feature

Contribution Overview

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Documents and Commands

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```
type Doc = { v:int };  
type Command = Doc→();  
  
val inc =  
  proc (d:Doc)  
    requires d≠null;  
    ensures d.v=old(d.v)+1;  
    { d.v:=d.v+1; };
```

```
var d:Doc; d:=new Doc; d.v:=41;  
inc(d);  
assert d.v=42;
```

Command Factories

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```
val createAdd =  
  proc (x:int) returns Command  
    ensures  $\underline{\forall} \cdot \forall d:\text{Doc} \cdot d \neq \text{null} \Rightarrow \text{pre}(\text{result}, d)$  ;  
    ensures  $\underline{\forall} \cdot \forall d:\text{Doc} \cdot$   
      post (result, d)  $\Rightarrow d.v = \text{old}(d.v) + x$  ;  
  { result :=  
    proc (d:Doc)  
      requires d  $\neq$  null ;  
      ensures d.v = old(d.v) + x ;  
      { d.v := d.v + x ; } ;  
  } ;
```

```
var d:Doc; d := new Doc; d.v := 40 ;  
var f := createAdd(2) ; f(d) ;  
assert d.v = 42 ;
```

Translation into Boogie

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- A state is a heap and a lexical environment $\sigma=(H;E)$
 - Env = Identifier \rightarrow Location
 - Heap = Location \rightarrow Value
- Each static procedure definition is given a unique *key*
 - defunctionalization
- A closure is a key and a lexical environment $c=(k;e)$
- Semantical functions *pre* and *post* (specification functions) correspond to syntax **pre/post**
- Specifications are translated into axioms

Axiomatization in Boogie

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```
val createAdd =  
  proc(x:int) returns Command  
    ensures  $\underline{\forall \cdot \forall d:\text{Doc} \cdot d \neq \text{null} \Rightarrow \text{pre}(\text{result}, d)}$ ;  
     $k_1$  ensures  $\underline{\forall \cdot \forall d:\text{Doc} \cdot}$   
      post(result,d)  $\Rightarrow d.v = \text{old}(d.v) + x$ ;  
  { result:=  
    proc(d:Doc)  
       $k_2$  requires  $d \neq \text{null}$ ;  
      ensures  $d.v = \text{old}(d.v) + x$ ;  
      { d.v:=d.v+x; };  
    };
```

$\forall c:\text{Closure}, d:\text{Doc}, h:\text{Heap} \cdot \text{key}(c)=k_2 \Rightarrow (\text{pre}(h, c, d) \Leftrightarrow d \neq \text{null})$

$\forall c, r:\text{Closure}, x:\mathbb{Z}, h, h':\text{Heap} \cdot \text{key}(c)=k_1 \Rightarrow$
($\text{post}(h', h, c, x, r) \Leftrightarrow \forall d:\text{Doc}, h_0:\text{Heap} \cdot d \neq \text{null} \Rightarrow \text{pre}(h_0, r, d)$)

Creation and Invocation in Boogie

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```
val createAdd = proc ...  $k_1$ 
```

```
havoc temp; assume key(temp)= $k_1$   $\wedge$  env(temp)=E;  
H[E[createAdd]] := temp;
```

```
f (d) ;
```

```
assert pre(H, H[E[f]], H[E[d]]);  
oH:=H; havoc H;  
assume post(H, oH, oH[E[f]], oH[E[d]]);
```


Dependency Cycles

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- Specifications about specifications may be inconsistent

```
val f = proc () requires  $\neg$ pre (f) ; {} ;
```

- Cycles are not detectable statically and modularly (recursion through store)

```
var x : ()  $\rightarrow$  () ;
```

```
val g = proc () requires  $\neg$ pre (x) ; {} ;
```

```
.....
```

```
x := g ;
```

Dependency Ordering

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- We introduce a *dependency ordering* $<$ on closures
 - $<$ is strict partial ordering (irreflexive, transitive relation)
 - the specification functions of closure c may depend on those of closure d iff $c < d$
- Convenient default for $<$
 - assume closure type T_2 is a result or parameter type of closure type T_1
 - assume $type(c)=T_1$ and $type(d)=T_2$
 - then $c < d$
 - procedure specifications typically mention their formal parameters and results
 - note: there are no infinite types!

Custom Dependency Ordering

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- Further axioms on \prec
 - introduced by the programmer

```
proc (...) returns ...  
    requires ...; ensures ...;  
    below d1,d2; above c1;  
    {...};
```

k_n

- checked for consistency

```
// consistency check  
assert init(H[E[d1]])  $\wedge$  init(H[E[d2]])  $\wedge$  init(H[E[c1]]);  
assert H[E[c1]]  $\prec$  H[E[d1]]  $\wedge$  H[E[c1]]  $\prec$  H[E[d2]];  
  
// closure creation  
havoc temp;  
assume key(temp)= $k_n$   $\wedge$  env(temp)=E;  
assume temp  $\prec$  H[E[d1]]  $\wedge$  temp  $\prec$  H[E[d2]]  $\wedge$  H[E[c1]]  $\prec$  temp;
```

Dependency Cycles Revisited

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```
val g = proc () requires  $\neg$ pre(x); {};
```

k_g

$\forall c:\text{Closure}, h:\text{Heap} \cdot \text{key}(c)=k_g \Rightarrow$
 $(\text{pre}(h, c) \Leftrightarrow \neg \text{if } c < h[E[x]] \text{ then } \text{pre}(h, h[E[x]]) \text{ else } \text{undefined})$

A while Loop

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```
val while =  
  proc (cond: () →f bool, body: () → (), inv: () →g bool )  
    requires  $\forall \cdot \text{pre}(\text{inv}) \wedge (\text{inv}() \Rightarrow \text{pre}(\text{cond}))$  ;  
    requires inv() ;  
    requires body()  $\sqsubseteq$  [inv()  $\wedge$  cond(), old(inv()  $\wedge$  cond())  $\Rightarrow$  inv()] ;  
    ensures inv()  $\wedge$   $\neg$ cond() ;  
  { if(cond())  
    { body(); while(cond, body, inv); }  
  } ;
```

ghost closure

Eratosthenes' Sieve Factory

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```
val eratosthenesFactory =  
  proc(limit:int) returns int→fbool  
    requires limit>1 ;  
    ensures  $\forall \cdot \forall n:\text{int} \cdot 1 < n \leq \text{limit} \Rightarrow (\text{result}(n) \Leftrightarrow \text{PRIME}(n))$  ;  
  {  
    var i:int;  
    var isPrime: int→fbool;  
    i:=2;  
    isPrime:=func(n:int) { true };  
    val inv = ...;  
    val body = ...;  
    while(func() {i≤limit}, body, inv);  
    result:=isPrime;  
  };
```

Eratosthenes' Sieve Factory: Loop Invariant

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```
val inv =  
  ghost() returns bool  
  below isPrime;  
{  
   $i \leq \text{limit} + 1 \wedge \text{inv} < \text{isPrime}$   
   $\wedge \forall j:\text{int}.$   
    pre( $\sim \text{isPrime}(j)$ )  
     $\wedge (1 < j \Rightarrow (\sim \text{isPrime}(j) \Leftrightarrow$   
       $\forall k:\text{int}. 2 \leq k < \sim i \wedge k < j \wedge \text{PRIME}(k) \Rightarrow \neg \text{DIVIDES}(j, k)))$   
};
```

Eratosthenes' Sieve Factory: Loop Body

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```
val body =  
  proc ()  
    requires inv()  $\wedge$  1 < i  $\leq$  limit;  
    ensures inv()  $\wedge$  i = old(i) + 1;  
    below inv;  
  {  
    val oIsPrime = isPrime;  
    val oI = i;  
    if(isPrime(i))  
    {  
      isPrime :=  
        func(n: int)  
          below oIsPrime; above inv;  
          {oIsPrime(n)  $\wedge$   $\neg$ DIVIDES(n, oI)};  
    }  
    i := i + 1;  
  };
```


...AND HERE IT IS WORKING...

Conclusion

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- **Methodology for modular and automated verification of programs with delegation**
 - first order logic specification language
 - proven consistent and sound (not shown here)
 - covers non-trivial examples
 - covers framing, abstraction, aliasing, sharing (not shown here)
- **Future Work**
 - implement in a verifier
 - use core ideas to design similar solution with symbolic execution and/or a permission logic (separation logic or implicit dynamic frames)